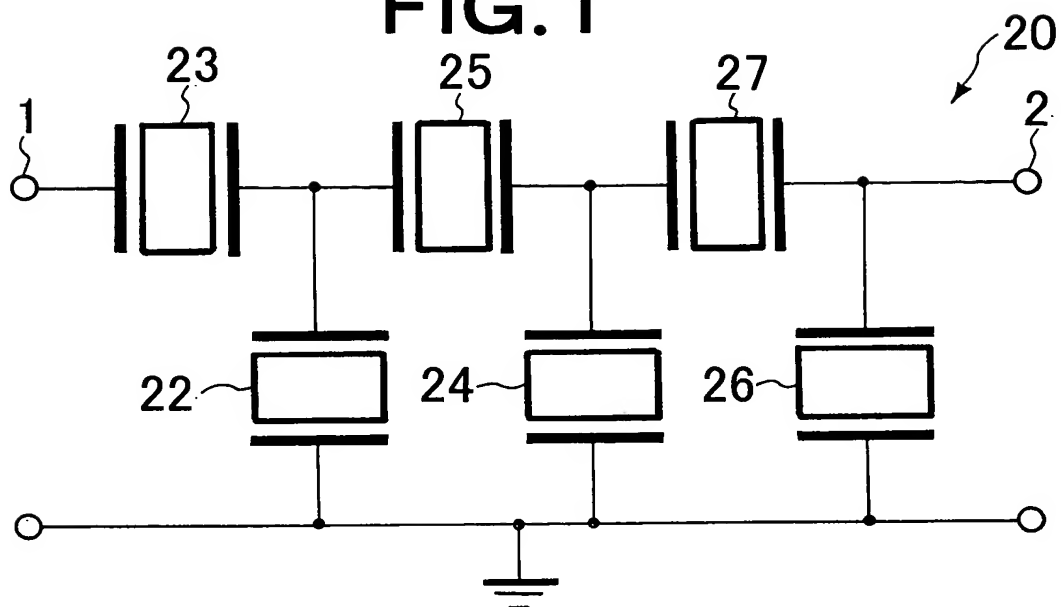
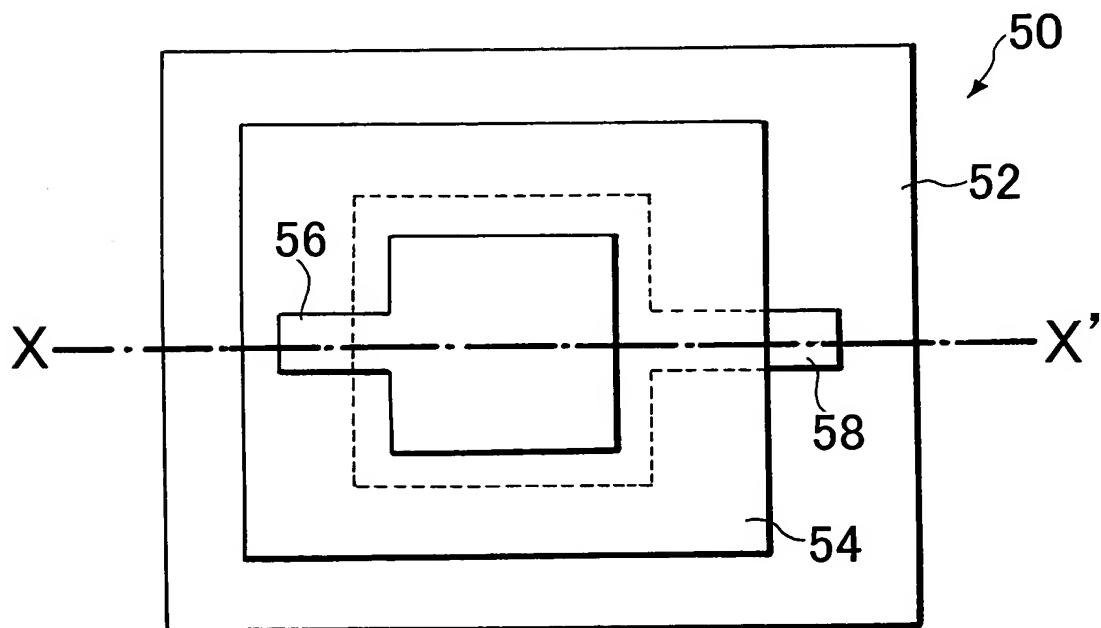


**FIG. 1**

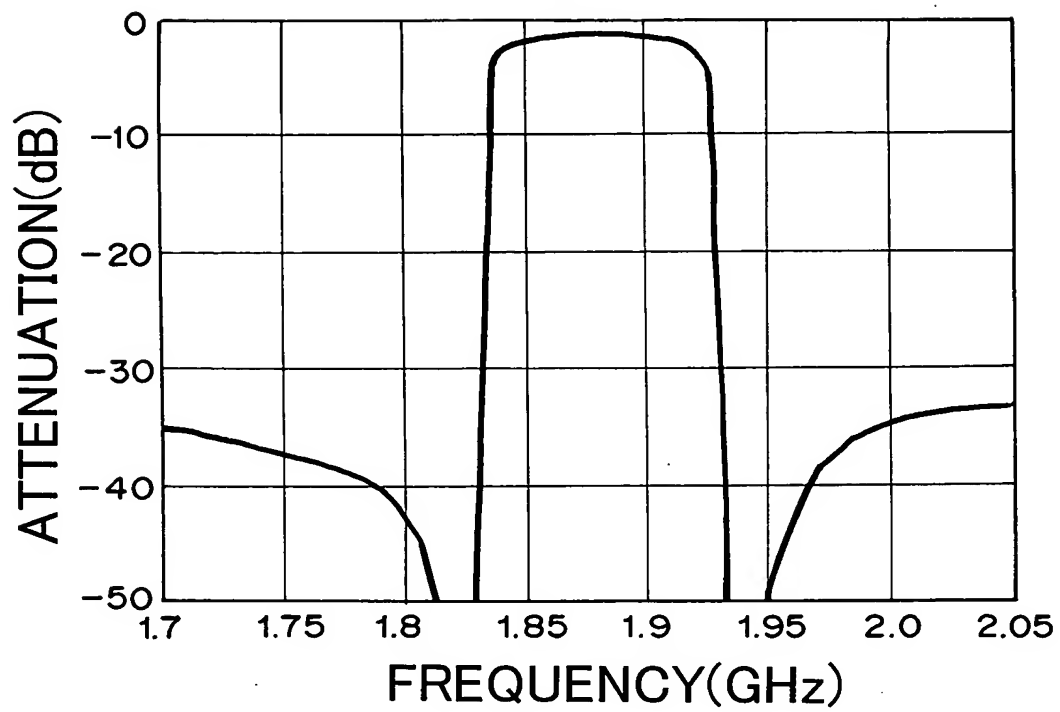


**FIG. 2**

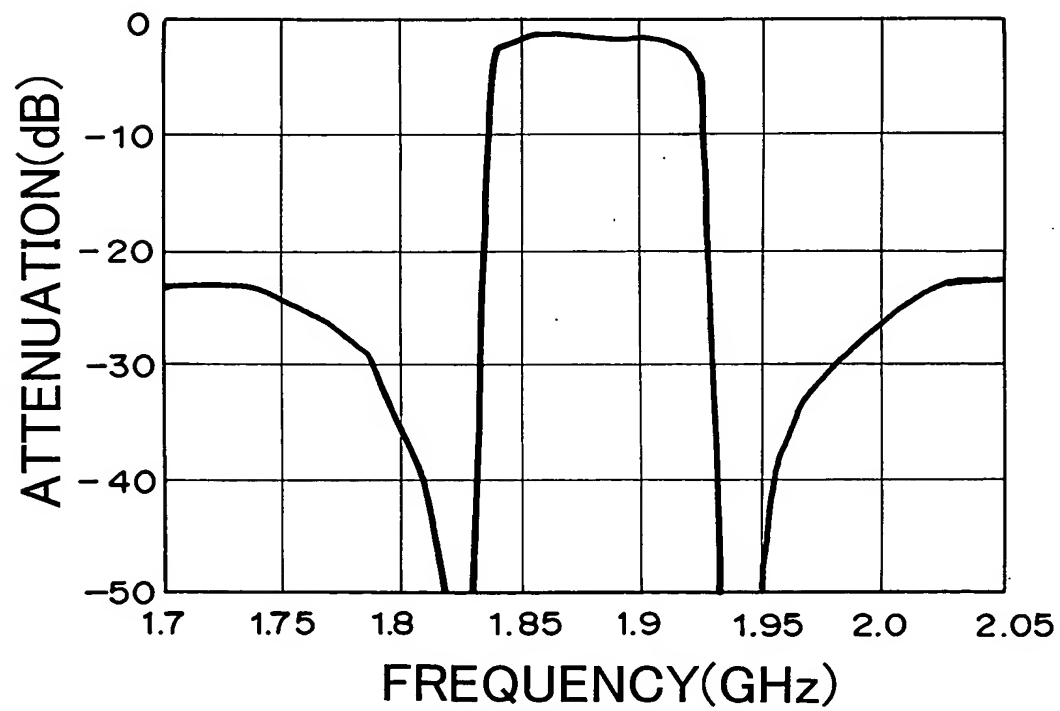


A schematic diagram of a semiconductor device 20. The device is enclosed in a rectangular frame 52. Inside the frame, there is a central region 54. Within this central region, there are five rectangular elements: 22, 23, 24, 25, and 26, 27. These elements are arranged in a grid-like pattern. The elements are connected to a grid 56 and a central region 58. The device is also connected to a central region 54. Labels 1 and 2 point to specific regions, and G points to a central region.

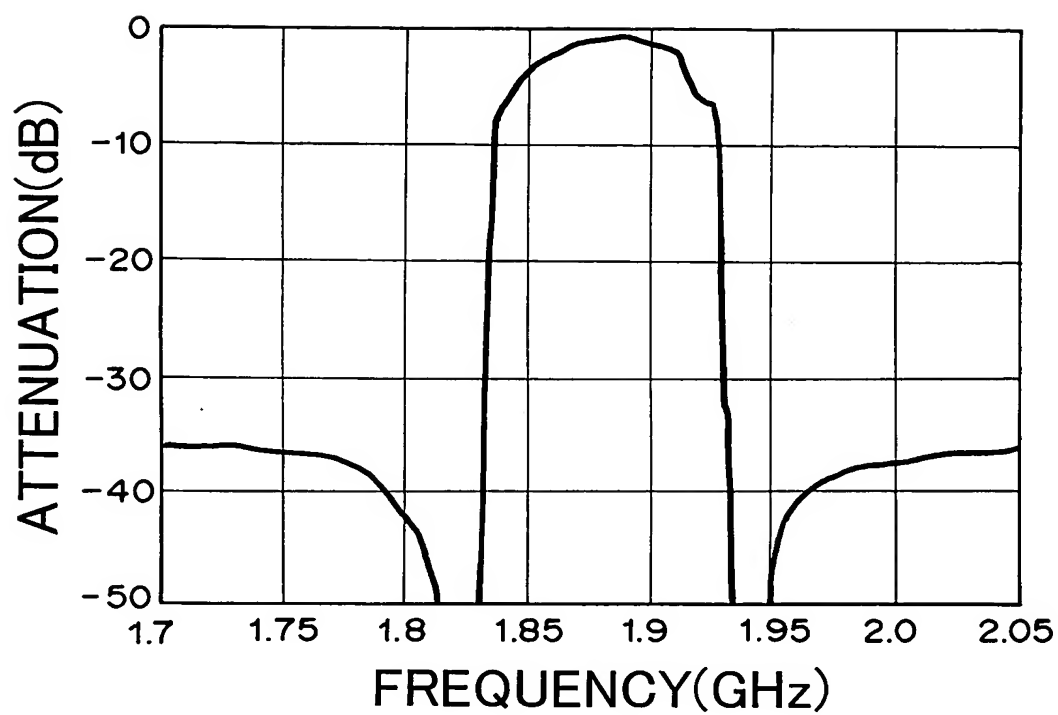
**FIG.5**



**FIG.6**



**FIG.7**



**FIG.8**

